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	INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	10/086,376
Date Submitted: June 13, 2002	Filing Date	02/5/2002	First Named Inventor	Igor KOUZNETSOV et al.
	Group Art Unit	2821	Examiner Name	Unassigned
	Attorney Docket Number	035905-0118		

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
LP	B1	4,500,905		Shibata		
	B2	6,185,122		Johnson et al.		
	B3	3,414,892		McCormack et al.	12/13/1968	
	B4	3,432,827		Sarno	3/11/1969	
	B5	4,535,424		Reid		
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	B15	5,266,912		Kledzik		
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Date Considered

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STATEMENT BY APPLICANT

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First Named Inventor	Igor KOUZNETSOV et al.
Group Art Unit	2821
Examiner Name	Unassigned
Attorney Docket Number	035905-0118

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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
LP	B60	EPO	0 073 486	A2	Toyama et al.	8-26-1982		
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Sheet	3	of	7

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS); title of the article (when appropriate); title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
LP	B71	ABOU-SAMRA S.J.: "3D CMOS SOI for High Performance Computing", Low Power Electronics and Design Proceedings, 1998.	
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Filing Date	02/5/2002
First Named Inventor	Igor KOUZNETSOV et al.
Group Art Unit	2821
Examiner Name	Unassigned
Attorney Docket Number	035905-0118

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	†
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LP ↓ ↓ ↓ ↓	B119	Patent Application, AKRAM, US 2002/0030262 A1.	
	B120	Patent Application, AKRAM, US 2002/0030263 A1.	
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LP ↓	C1	6,110,278		Saxena	8/29/2000	
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	C6	0028541A1		Lee et al	03/07/2002	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)				

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
LP	C3	"3D Chip-On-Chip Stacking," Semiconductor International, December 1991	
LP	C4	RICHARD W. LAY: "TRW Develops Wireless Multiboard Interconnect System," Electronic Engineering Times, November 5, 1994	

Examiner Signature	Long Pham	Date Considered	7/22/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Substitute for form 1449B/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	10/068,376
Date Submitted: April 5, 2002		Filing Date	February 5, 2002
(use as many sheets as necessary)		First Named Inventor	Igor KOUZNETSOV et al.
Sheet 1 of 1		Group Art Unit	2823
		Examiner Name	Unassigned
		Attorney Docket Number	035805-0118

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code* (if known)			
LP	A127	6,208,645		Leedy	3/27/2001	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²
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